



Shri Vile Parle Kelavani Mandal's

DWARKADAS J. SANGHVI COLLEGE OF ENGINEERING

(Autonomous College Affiliated to the University of Mumbai)

NAAC Accredited with "A" Grade (CGPA : 3.18)



REGULAR EXAMINATION TIMETABLE, JANUARY, 2025
MASTER OF TECHNOLOGY (M. Tech.)
SECOND YEAR, SEMESTER – III (For 2024 Batch Under Autonomy)
Timings: 10.00 a.m. to 01.00 p.m.

Date: 10th December, 2024

Day & Date	Computer Engineering
Tuesday, 21/01/2025	NPTEL Credit Course: Cryptography and Network Security

Important Note: Changes if any, in the timetable, shall be communicated on the College website (djsce.ac.in).

Ms. Alka Shukla
Controller of Examinations

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Dr. Hari Vasudevan
Principal

11/12/24